Docket No. 44176.00033



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

4/21/03 Amith

## CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231, on

Date: April 3, 2003

In re Application of

Serial No.

Ejaz Ul Haq

09/851,622

Filing Date: May 8, 2001

For: HIGH SPEED SOURCE SYNCHRONOUS

> SIGNALING FOR INTERFACING VLSI CMOS CIRCUITS TO TRANSMISSION

**LINES** 

Examiner: Dinh Le

Group Art Unit:

2816

Commissioner for Patents Washington, D.C. 20231

## AMENDMENT AND RESPONSE WITH PETITION TO REVIVE

Sir:

In response to the office action of June 19, 2002 and accompanying a petition to revive for unintentional abandonment and a continuation application, please amend the application as follows:

RECEIVED

APR 0 92003

**OFFICE OF PETITIONS** 

In re Haq U.S. Patent Application No.: 09/851,622

Page 1 of 13 PaloAlto Doc #49139v2